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# Advanced Design of Current-mode Pass-band Filter using Ant Colony Optimization Technique

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ARTICLE INFO	ABSTRACT
Article history: Received: 02 September, 2020 Accepted: 02 November, 2020 Online: 08 December, 2020	Ant Colony Optimization (ACO) algorithm, a well-known robust technique to solve easily both a simple and multiple objective optimization problems. This article presents an application of the ACO in order to achieve the optimal sizing of analog circuit. The proposed technique is employed to optimizing the sizing of a positive second-generation where the provide shows here the structure the provide show the second
Keywords: Ant colony optimisation Analog circuit design Positive second-generation current conveyor	by other optimization procedure. PSPICE simulations are used to confirm the validity of the reached optimal results and the accuracy of the proposed procedure.

# 1. Introduction

Current Conveyor (CC) represents the first construction block developed for current signal processing [1]. Two years later, in 1970, another variant of a CC, namely, Second Generation Current Conveyor (CCII) was published [2].

Recently, a Current Conveyor is seen as a good alternative for the voltage-mode. CC can be widely employed in different applications. The CCII is the most popular configuration [3].

Despite the number of positive proprieties ensured by the CCS [4], it seems that designing integrated CC circuits with high performance is in effect still open particularly in CMOS technology [5].

Currently, an important interest has been devoted to achieve the best sizing of the analog component efficiently and with high accuracy. Some (meta) heuristics were used in the scientific publications such as Genetic Algorithms (GA) [6, 7], Ant Colony Optimization (ACO) [8, 9], Particle Swarm Optimization (PSO) [10], Differential Evolution algorithm (DE) [11], Simulated Annealing (SA) [12] and Artificial Bee Colony Algorithm (ABC) [13, 14].

ACO algorithm is proposed in this work, with view to get an optimal design of positive second-generation current conveyor (CCII+). To this end, we applied three of the most important

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variants of ACO, specifically, the Ant System (AS), the Min-Max Ant System (MMAS) and the Ant Colony System (ACS). In fact, the ACO has already efficiently been used to treat real optimization problems, particularly in the analog circuit area.

A high performance CCII+ depends on the high-end cut-off frequency for the current waveform ( $F_{ci}$ ) and the input impedance at port X. Hence, two objectives functions are taken into consideration: achieve a low input resistance  $R_X$  and a very high cut-off frequency  $F_{ci}$ . However, the suggested technique is a mono-objective one, to this end; the optimization process is treated using two distinct manners; firstly, each objective function is solved separately. In the second way, a mono-objective problem is considered by using a weighting technique.

This paper is organized in the following manner: The second section is about a detail description of the ACO technique. The third section proposes an example for the optimal sizing of CCII+. Then, a comparison with some similar works and simulation results are represented in the fourth section. Through the fifth section, the optimized CCII+ is exploited in order to form a current mode filter. Finally, the sixth section is the conclusion.

#### 2. The ACO Technique Presentation

Ant Colony Optimization (ACO) is a nature-inspired algorithm, used to resolve various hard problems. The use of ACO has proved a capacity to deal with complex optimization problems successfully, for instance we can cite the traveling salesman problem (TSP) [15], vehicle routing problem [16], the quadratic assignment problem (QAP) [17].

The ACO takes inspiration from the real ant colonies behavior. The fundamental idea of this metaheuristic consists in simulating the social intelligence of ants in searching the shortest road from the nest and a source of food. In fact, real ants communicate indirectly through trails of chemical pheromone. Concretely, the agents of the colony deposit pheromone on the road to determine certain favorable route to be pursued by other ants [18].

# 2.1. Ant System

« Ant System » (AS) is the basic variant of the ACO. The modelling approach to resolve a given optimization problem is established as follows:

Ants choose the vertex to travel to by applying the state transition rule [19, 20]. Ant k in node a will move to vertex b according to the probability offered by (1):

$$P_{ab}^{k} = \begin{cases} \frac{(\tau_{ab})^{\alpha} \cdot (\eta_{ab})^{\beta}}{\sum_{l \in J_{a}^{k}} (\tau_{ab})^{\alpha} \cdot (\eta_{ab})^{\beta}} & \text{if } a \in J_{a}^{k} \\ 0 & \text{if } a \notin J_{a}^{k} \end{cases}$$
(1)

while  $J_a^k$  corresponds to the list of possible moves, which are possible for ants starting form vertex a,  $\tau_{ab}$  represents the intensity of the trace associated to brink (a, b).  $\alpha$  and  $\beta$  identify the relative impact of the trace intensity and the visibility value,  $\eta_{ab}$ .

where

$$\eta_{ab} = \frac{1}{d_{ab}} \tag{2}$$

The  $d_{ab}$  represents the distance between nodes a and b

The pheromone is updated by means of formula (3), when all agents of the colony construct their path:

$$\tau_{ab} = (1 - \rho) \cdot \tau_{ab} + \sum_{k=1}^{m} \Delta \tau_{ab}^{k}$$
(3)

While  $\rho$  corresponds to the pheromone decay parameter, m represents the number of ants, and  $\Delta \tau_{ab}{}^{k}$  is the amount of pheromone deposited by ant k on brink (a, b):

$$\Delta \tau_{ab}^{k} = \begin{cases} \frac{Q}{L_{k}} & \text{if } (a,b) \text{ belongs to tour done by k} \\ 0 & \text{otherwise} \end{cases}$$
(4)

Q is a constant;  $L_k$  represents the tour length built by ant k.

# 2.2. Min-Man Ant System

# The Min-Max Ant System is an adjustment of the AS [21].

Min-Max Ant System (MMAS) is designed to attain the best result. To this end, only the optimal result is authorized to intensify the pheromone quantity. In fact, MMAS uses an instrument to border the trace intensity, and so that avoid the premature stagnation. MMAS presents some new features and they are as follows:

- Only the ants that produced the best solution are allowed to update trails;
- To avoid stagnation, MMAS introduces upper and lower bounds so that the pheromone intensities lie inside the interval [τ<sub>min</sub>, τ<sub>max</sub>];
- In each brink, the trail strength is initialized to  $\tau_{max}$ .

# 2.3. Ant Colony System

The Ant Colony System (ACS) algorithm incorporates three main differences with respect to the AS algorithm:

• ACS algorithm uses a parameter q₀ (0≤q₀≤1), which controls the tuning between exploitation and exploration. thus, the probability that ant moves to node b is given by this rule:

$$b = \begin{cases} \arg\max_{u \in B_a^k} = \left[ \left( \tau_{au}(t)^* (\eta_{aB})^{\beta} \right) \right] \text{ if } q \le q_0 \\ (1) \qquad \text{ if } q \succ q_0 \end{cases}$$
(5)

where q is a random variable evenly distributed in [0, 1]

• The global pheromone update is applied only to edges that form part of the best solution, as in the following:

$$\tau_{ab} = (1 - \rho) \cdot \tau_{ab} + \rho \cdot \Delta \tau_{ab}$$
(6)

where

$$\tau_{ab} = \begin{cases} \frac{1}{L} & \text{if } (a,b) \in best - global - tour \\ 0 & \text{otherwise} \end{cases}$$
(7)

• The updating of local pheromone is applied when each ant produce a solution:

$$\tau_{ab} = (1 - \rho) \cdot \tau_{ab} + \rho \cdot \tau_{int}$$
(8)

The procedure of the ACO technique can be illustrated by means of the flowchart bellow:

#### 3. Application of the ACO to the optimal sizing of CCII+

CCII is the most popular configuration of CCs; it replaces the conventional OPAMP in different applications as for example active filters, oscillators, and analog signal processing.

The CCII is three terminal devices; the current conveyor's response is illustrated by expression (9) [22]:

$$\begin{pmatrix} I_{Y} \\ V_{X} \\ I_{Z} \end{pmatrix} = \begin{pmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 \pm \beta & 0 \end{pmatrix} \begin{pmatrix} V_{Y} \\ I_{X} \\ V_{Z} \end{pmatrix}$$
(9)

The voltage at node X is duplicated from the voltage applied at node Y. Similarly, the current at Z port is copied from the current flowing through node X. A positive Z output current conveyor is considered when  $\beta = +1$  and it is denoted CCII+. For optimal

circuit performance, it is desirable to have very high impedance on Y and Z nodes and small Rx, i.e. the resistance on X [22].



Figure 1: Flowchart of ACO

In this work, the ACO is used to optimize the performances of a positive second-generation CMOS current conveyor (CCII+) (figure2). Optimizing the most affecting parameters of the current conveyor is highlighted:

This optimization problem is resolved using two methods; in the first one, two mono objective function are considered. The objective functions are:

• Minimize the Rx, which represents the X-port input parasitic resistance

$$R_x \approx \frac{1}{gm_2 + gm_4} \tag{10}$$

Maximize the Fci, which represents the current high cut off frequency

$$F_{ci} = \frac{1}{2\Pi} \sqrt{\frac{gm_5(gm_4 + go_4)}{Cgs_4(Cgs_5 + Cgs_6 + Cgd_4)}}$$
(11)



Figure 2: A positive second generation current conveyor

In the second method, the proposed objective function allows simultaneously minimizing Rx and maximizing Fci, and it is configured as:

$$f_{obj} = \frac{1}{F_{ci}} + R_X \tag{12}$$

The MOS transistors size represents the geometric variables that will be considered: the gates widths (WN and WP) and channels lengths (LN, and LP). The binding functioning conditions require that all the transistors of CCII+ must function in the saturation mode. So that, the main constrain of the optimization problem are presented by expressions (13) and (14):

$$\frac{V_{DD}}{2} + V_{TP} + \sqrt{\frac{2I_0}{\mu_N C_{ox} \frac{W_2}{L_2}}} \ge \sqrt{\frac{2I_0}{\mu_P C_{ox} \frac{W_8}{L_8}}}$$
(13)

$$\frac{V_{DD}}{2} - V_{TN} - \sqrt{\frac{2I_0}{\mu_N C_{ox} \frac{W_5}{L_5}}} \ge \sqrt{\frac{2I_0}{\mu_P C_{ox} \frac{W_4}{L_4}}}$$
(14)

where  $C_{ox}$ ,  $\mu_N$ , and  $\mu_P$  are MOS technology parameters.  $V_{TN}$  and  $V_{TP}$  denote threshold voltages of NMOS and PMOS transistors respectively. The employed supply voltages are  $V_{SS}/V_{DD}$ = -2.5V/2.5 V and a bias current, Ibias=100  $\mu$ A.

#### 4. Results and Discussion

The proposed variants of the ACO technique were applied to deal with the optimal sizing of CCII+. The optimization technique works on C++ codes.

# 4.1. Optimization problem result using tow separated objective functions

The optimal values for the parameters of CCII+ achieved by the ACO algorithms and the circuit's performances linked with these values are indicated in tables 1 and 2. Table 3 presents the performances provided by the AS, the MMAS and the ACS techniques versus to the ones ensured by Particle Swarm Optimization (PSO) and the Genetic Algorithm (GA) algorithms.

	LN	WN	LP	WP	R <sub>X-min</sub>	(Ω)
	(µm)	(µm)	(µm)	(µm)	Opt.	Sim.
AS	0.59	36.60	0.35	58.64	333.01	315.89
MMAS	0.60	36.34	0.35	59.00	335.63	318.69
ACS	0.59	36.68	0.35	59.00	332.65	315.19

Table 1: the obtained results for RX

Table 2: the obtained results for Fci						
	LN	WN	Fci-m	ax (GHz)		
	(µm)	(µm)	(µm)	(µm)	Opt.	Sim.
AS	0.55	5.48	0.35	9.52	1.795	1.739
MMAS	0.55	5.60	0.35	9.60	1.775	1.731
ACS	0.55	5.44	0.35	9.50	1.801	1.754

The results of the simulation under SPICE for Rx and Fci employing the optimal results acquired by the suggested techniques for CCII+ are exposed in figures 3 and 4. We can observe that simulation results are in good correspondence with theoretical values achieved by the ACO technique.



Figure 3: Variation of resistance (ohm) versus frequency (Hz) employing AS,MMAS and ACS



Figure 4: Current gain (dB) versus frequency (Hz) employing AS,MMAS and ACS

Table 3: Comparison results

	ACS	PSO[23]	GA[23]	
$R_{X}$ -min ( $\Omega$ )	332.65	464	510	
F <sub>ci</sub> - <sub>max</sub> (GHZ)	1.801	1.751	1.541	

It is shown that the three variants of the ACO algorithm offer similar results. The comparison with two techniques belonging to metaheuristics indicates that the ACS accomplished the greatest results with regard to objectives compared to Particle Swarm Optimization (PSO) and the Genetic Algorithm (GA) algorithms.

# 4.2. Optimization problem result using weighting approach:

The optimal physical parameters values of MOS transistors achieved employing the three variants of ACO algorithm are given in table 4. The comparison of the acquired results with those obtained by another metaheuristic namely, particle swarm optimization (PSO) algorithm, shows that ACS could reach a greatest  $F_{ci}$  value. However, PSO could achieve a smaller  $R_X$  value.

Table 4: Optimal parameter	er values and optimization	n results for $R_X$ and Fci
rable 4: Optimal paramete	er values and optimization	if results for K <sub>X</sub> and FCI

	LN	WN	LP	WP	F <sub>ci-max</sub>	R <sub>X-min</sub>
	(µm)	(µm)	(µm)	(µm)	(GHz)	$(\Omega)$
AS	0.58	7.95	0.35	13.00	1.446	711.42
MMAS	0.59	8.08	0.35	13,00	1.420	708.75
ACS	0.55	7.53	0.35	12.97	1.531	721.08
PSO-2S [24]	0.52	14.30	0.35	23.62	1.358	444

The following table presents the simulation results:

Table 5: The optimal results for Rx and Fci

	F <sub>ci_max</sub>		R <sub>X_min</sub>	
	(GHz)			(Ω)
	Opt.	Sim.	Opt.	Sim.
AS	1.446	1.495	711.417	825.896
MMAS	1.420	1.479	708.75	825.896
ACS	1.531	1.552	721.085	827.704



Figure 5: Current gain (dB) as a function of frequency (Hz) employing AS,MMAS and ACS

The figure 5 gives the SPICE simulation for current gain (in dB) as a function of frequency, and the figure 6 presents the SPICE simulation results for  $R_X$  (in ohm) as a function of frequency and this by employing the acquired optimal values by the three proposed variants of ACO algorithm for CCII+.

# 5. A Current Mode Filter based on the CCs

A second order filter can be implemented based on the use of simulated inductance [25]. In fact, Current conveyors present the basis of simulated inductance topology. Figure 7 shows a current mode active filter constituted of 4 current conveyors and two capacitors [26].



Figure 6: Variation of Rx (ohm) as a function of frequency (Hz) employing AS,MMAS and ACS



Figure 7: Current- mode pass-band filter

The quality factor and the resonance frequency of this filter are respectively presented by:

$$Q = \frac{R_G}{\sqrt{R_{X1}R_{X2}}} \sqrt{\frac{C_1}{C_2}}$$
(15)

$$f_0 = \frac{1}{2\pi} \frac{1}{\sqrt{R_{X1}R_{X2}C_1C_2}}$$
(16)

where:

$$\frac{1}{R_G} = \frac{1}{R_{X1}} + \frac{1}{R_{X2}} + \frac{1}{R_{X3}} + \frac{1}{R_{X4}}$$
(17)

 $R_{X1}$ ,  $R_{X2}$ ,  $R_{X3}$  and  $R_{X4}$  represent the parasitic resistances on port X of the considered CCII+.

The considered filter is simulated using SPICE, and it is designed based on the optimized CCII+ with optimal sizes determined by ACS for this cases:

- a. Firstly, it is designed by employing the obtained CCII+ offering the maximum  $F_{ci}$ .
- b. Secondly, it is designed by employing the obtained CCII+ offering the minimum  $R_X$ .
- c. Finally, it is designed by employing the obtained CCII+ offering simultaneously maximum  $F_{ci}$  and minimum  $R_X$ .

The optimal parameters values of MOS transistors for the three cases are summarized in the table 6.

	Minimum R <sub>X</sub>	Maximum F <sub>ci</sub>	Minimum R <sub>X</sub> and Maximum F <sub>ci</sub>
WN/ LN (µm)	36.68/0.59	05.44/0.55	7.53/0.55
WP/ LP (µm)	59.00/0.35	09.50/0.35	12.97/0.35

Table 6: Optimal physical parameters values of MOS transistors

Figure 8 shows the SPICE simulation of the filter gain, where C1=C2=0.1 pF.



Figure 8: Resonance frequency of the second order current mode band-pass filter

The resonance frequencies reached by the ACS are presented in the following table; we can observe that the best resonance frequency of the current mode second order band pass filter is recorded in the third case.

Table 7: The obtained resonance frequencies

	R <sub>X-min</sub>	F <sub>ci-max</sub>	$\begin{array}{c} R_{X\text{-min}} \text{ and } \\ F_{\text{ci-max}} \end{array}$
f <sub>o</sub> (MHz)	497.896	641.389	647.781

# 6. Conclusion

In this work, the Ant System (AS), the Min-Max Ant System (MMAS) and the Ant Colony System (ACS), which are the most important variants of Ant Colony Optimization technique, are applied in order to optimize the sizing of conventional CMOS CCII circuit. The performances of a CCII+, especially the current high cut off frequency and the X-port parasitic resistance are optimized. The acquired results prove that the ACO algorithm offers a competitive optimizing capability for the design of CCII+. Then, the improved CCII+ structure was utilized as an elementary

block to build current mode band -pass filter. SPICE simulations and comparison with already published works were highlighted.

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